

FIG. 1

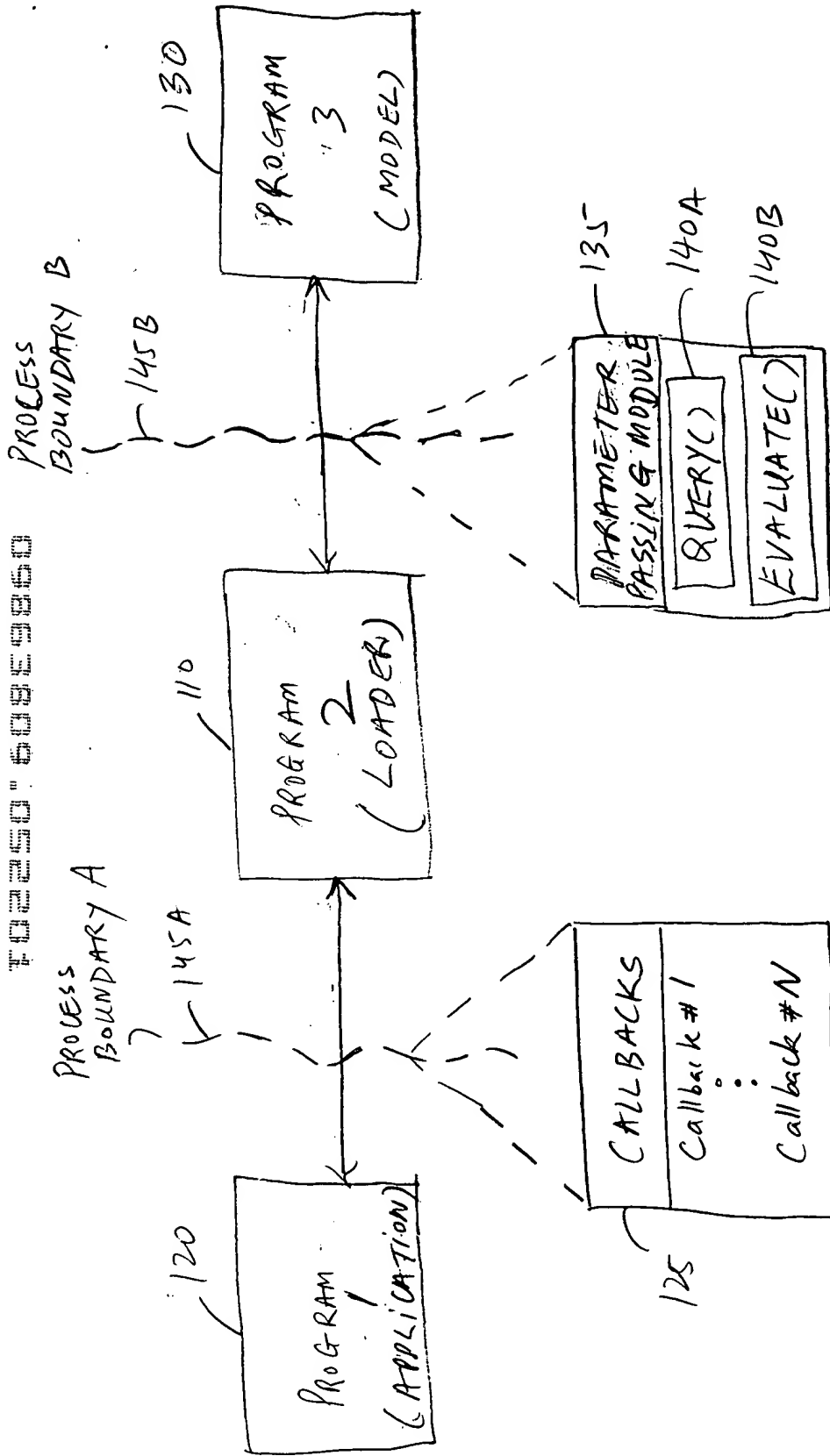


FIG. 1

100

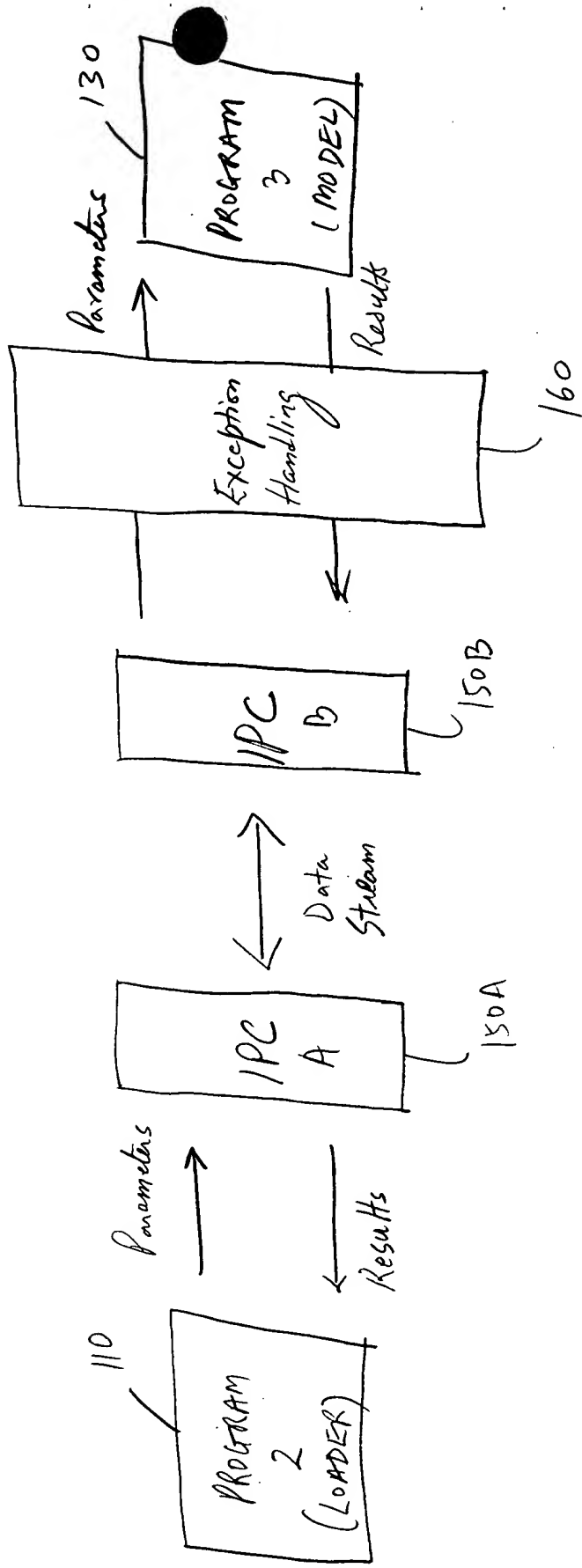


FIG. 2

MAPPED VALUE ARRAY A
 MAPPED TOGETHER VALUE ARRAY B
 MAPPED VALUE ARRAY C

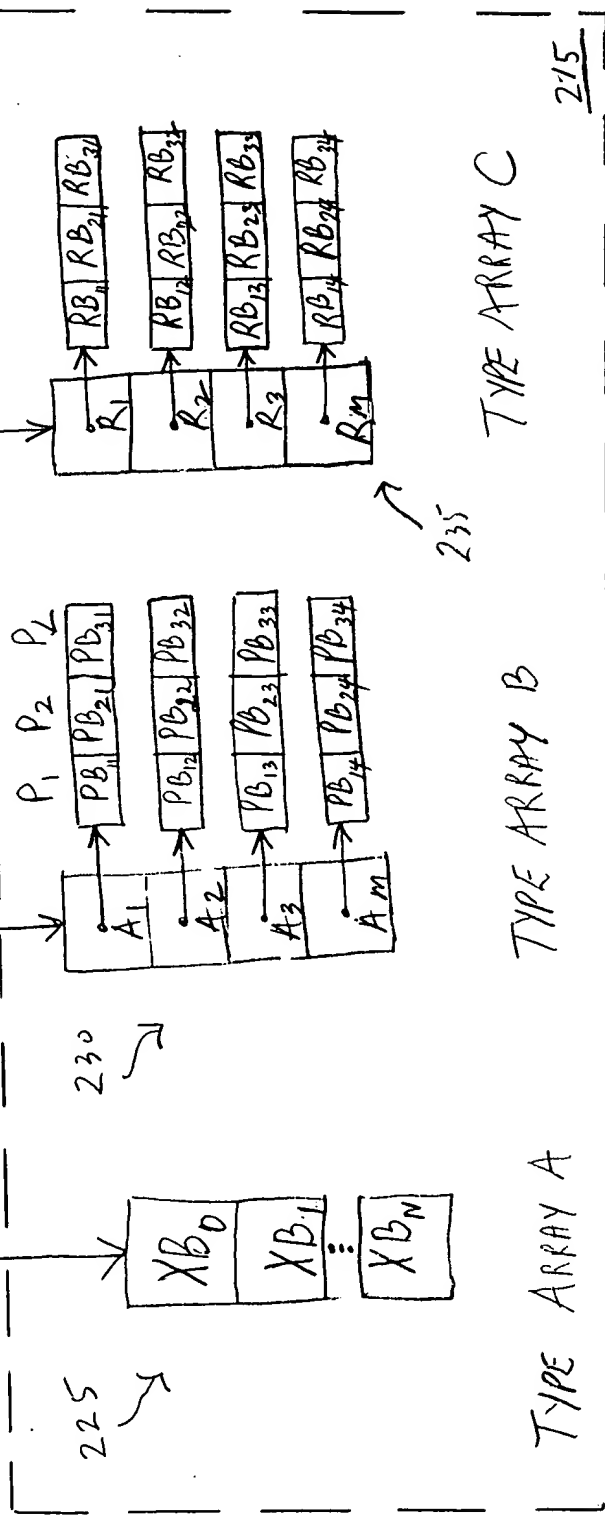
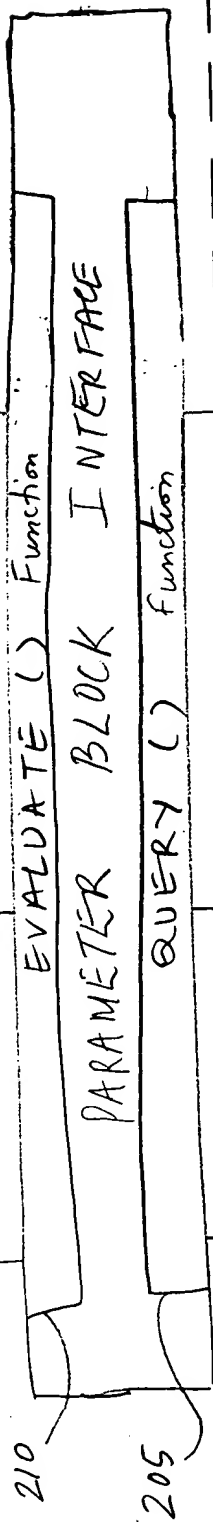
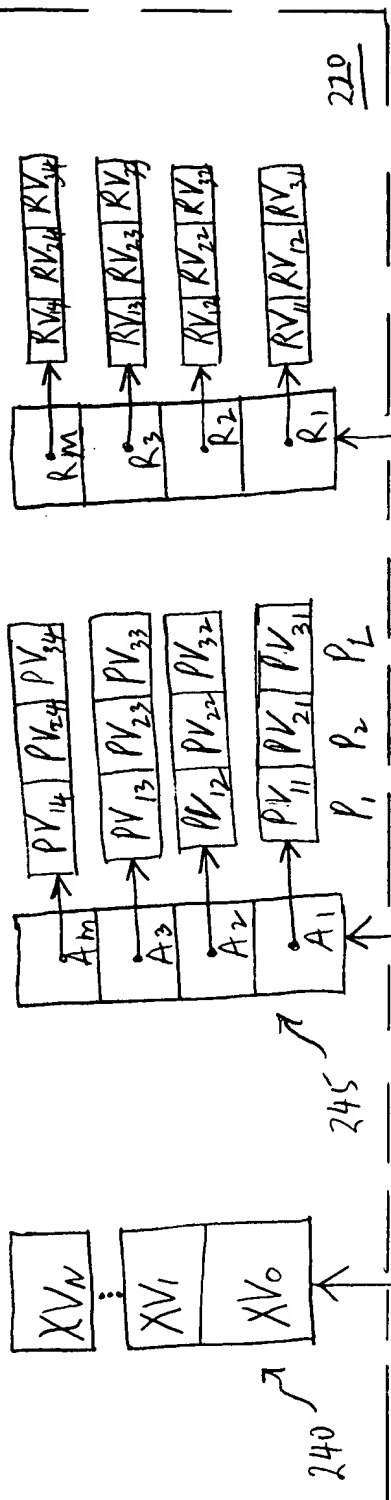


FIG. 3

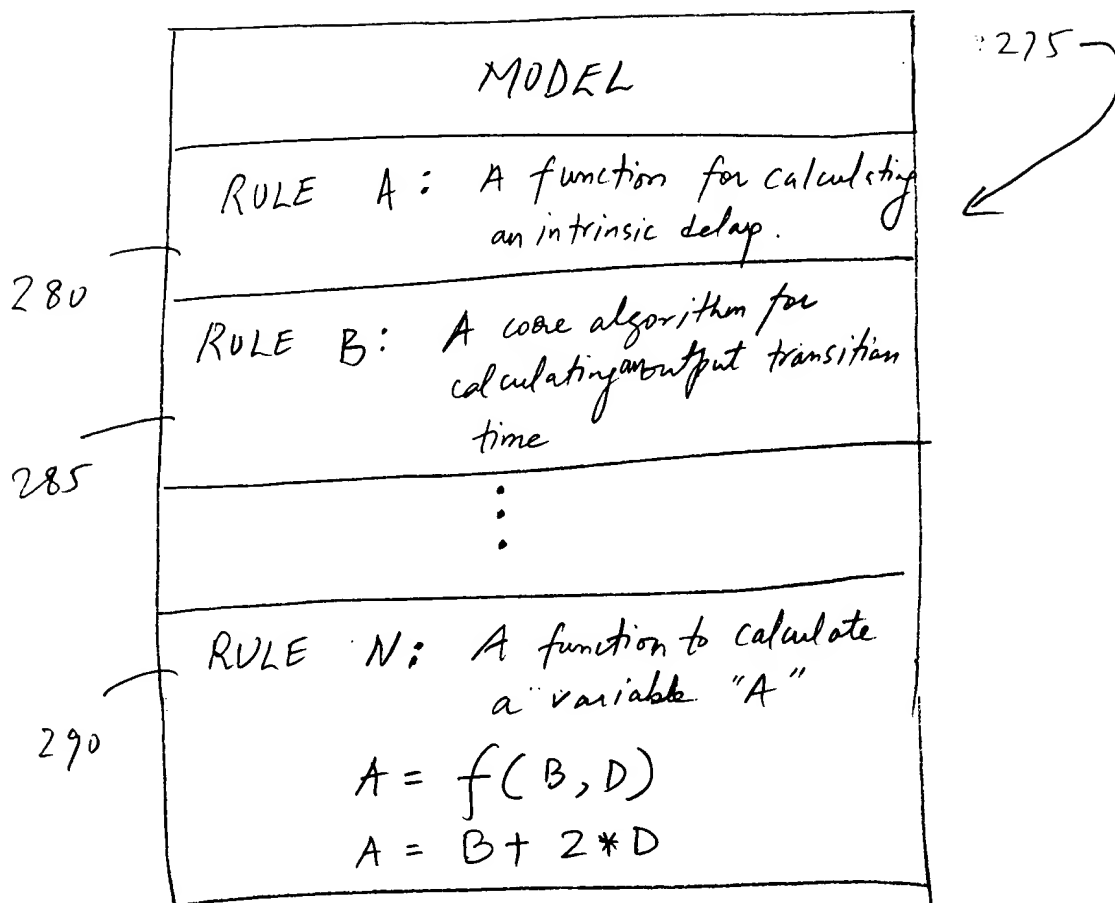


FIG. 4

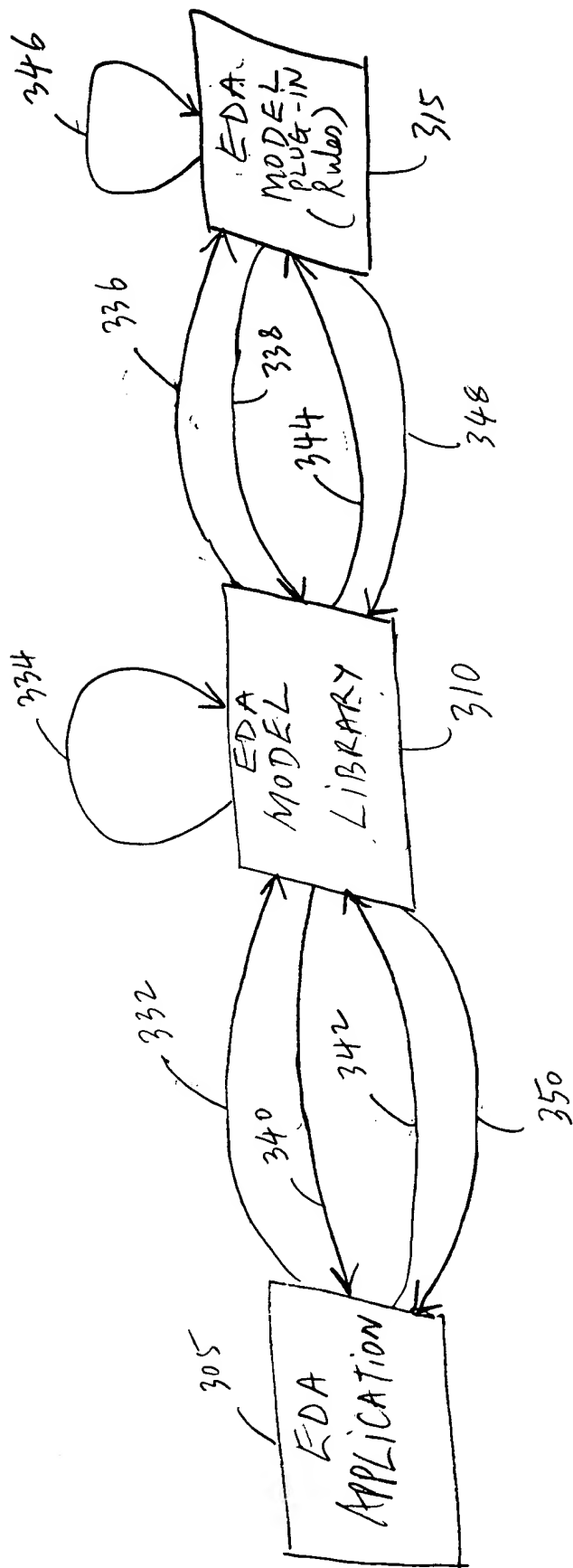


FIG. 5

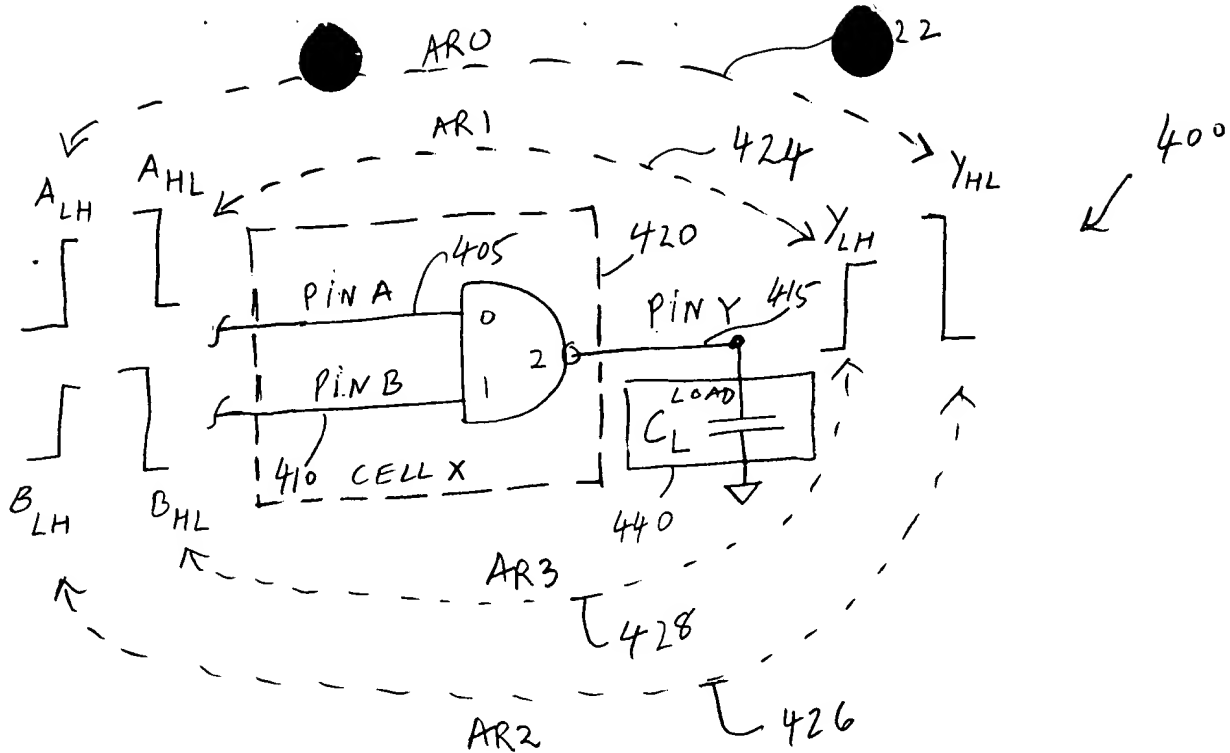


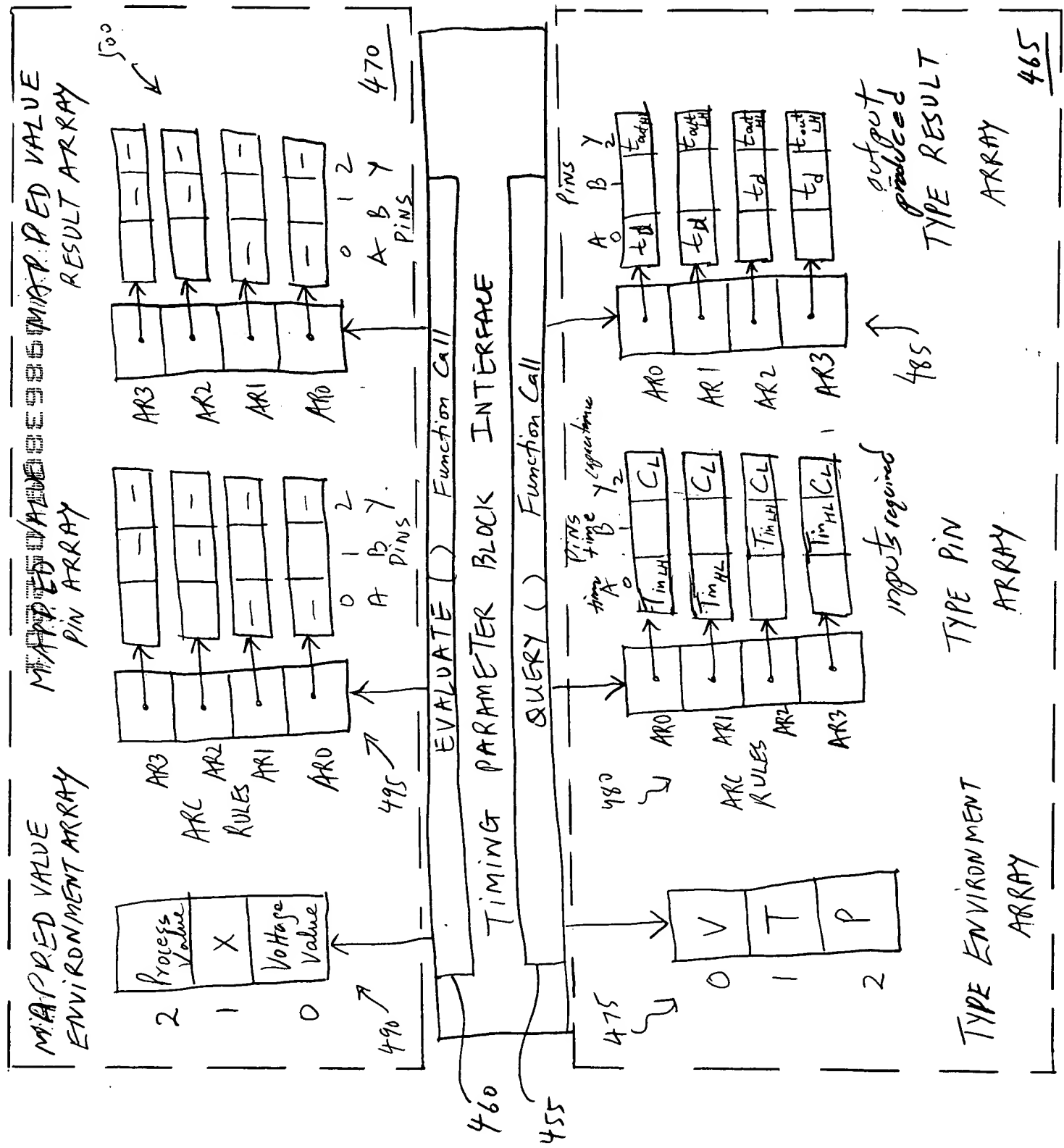
FIG. 6A

09863809.052201

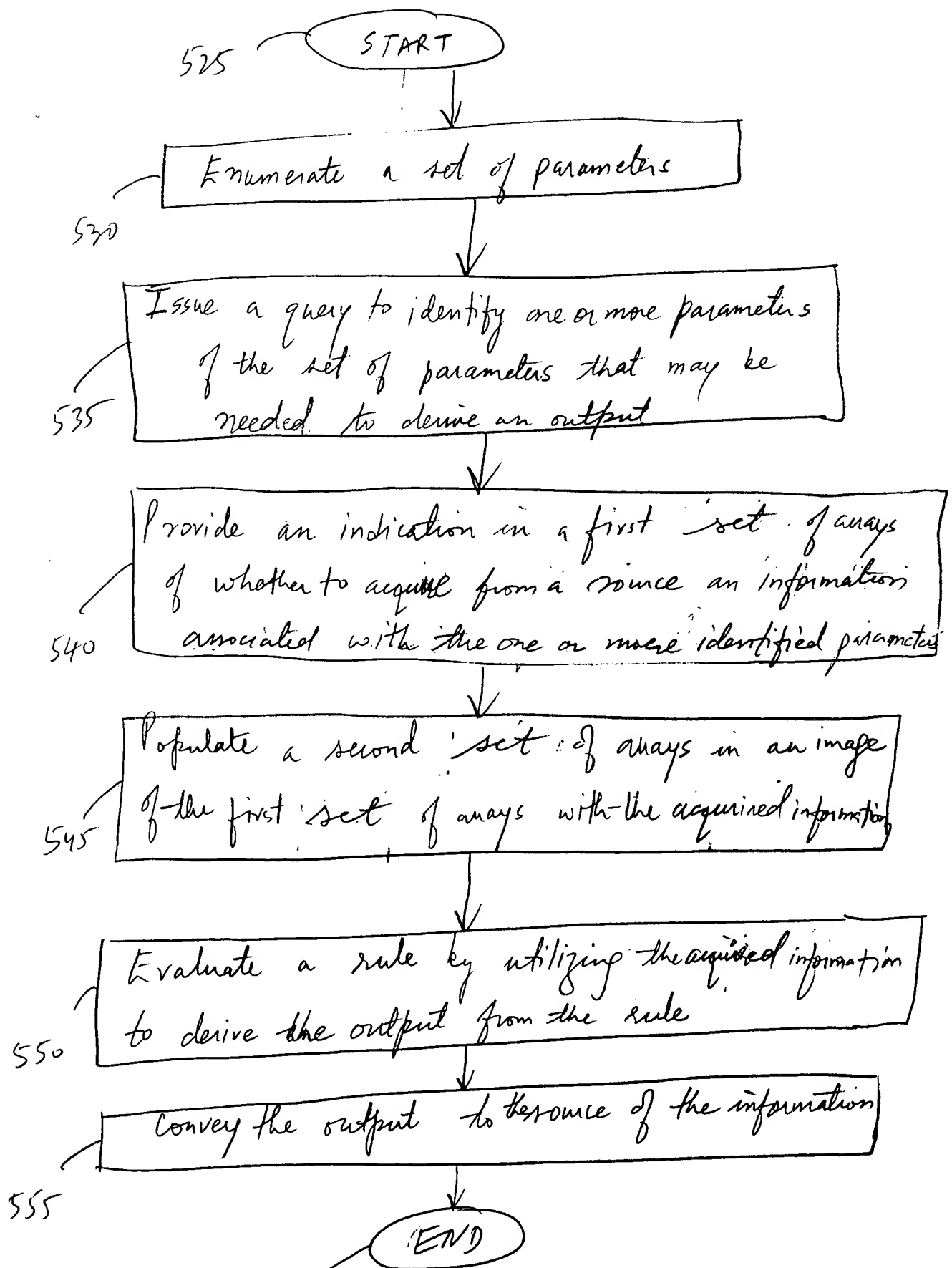
ARC RULES	INPUT A.T.		OUTPUT A.T.
	PIN A	PIN B	PIN Y
422 AR0	A _{LH}	1	Y _{HL}
424 AR1	A _{HL}	1	Y _{LH}
426 AR2	1	B _{LH}	Y _{HL}
428 AR3	1	B _{HL}	Y _{LH}

FIG. 6B

FIG. 7



450
↓



09863809 052203
FOUO 608E9860

560.

FIG-8A

09063809.052201

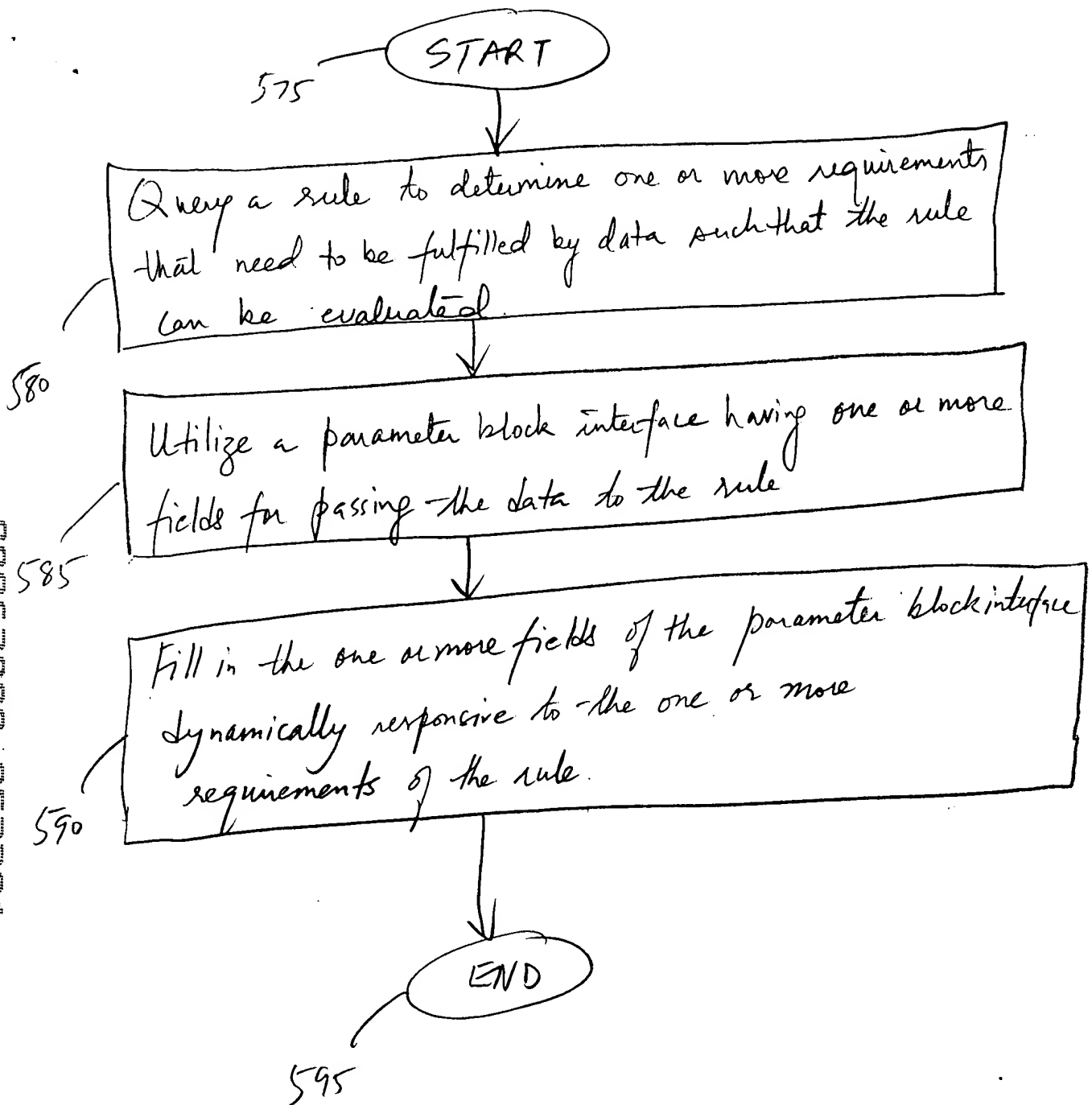


FIG. 8B